

INFORMATION DISCLOSURE STATEMENT	Case Name:	U. Koc 3-1	<i>10789681</i>
	Serial No.		
	Applicant:	U. Koc, et al.	
	Filing Date:	February 28, 2004	
	Group:		

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
<i>HFW</i>	AA	4,509,037	Harris	340	347	12/1/1982

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Subclass	Translation

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

<i>HFW</i>	AB	Anderson, R., "Getting the Most out of Delta-Sigma Converters," published online, March 2003, at: www.analogzone.com/acqt0130.pdf , 7 pages.
<i>HFW</i>	AC	Maurino, R., "A 200-MHz IF 11-bit Fourth-Order Bandpass $\Delta\Sigma$ ADC in SiGe," IEEE Journal of Solid-State Circuits, Vol. 35, No. 7, pp. 959-967, July 2000.
<i>HFW</i>	AD	Cherry, J.A., et al., "Continuous-Time Delta-Sigma Modulators for High-Speed A/D Conversion," Kluwer Academic Publishers (2000) pp. vii-ix and 1-29. <i>No money</i>
<i>HFW</i>	AE	Ritoniemi, T., et al., "Design of Stable High Order 1-Bit Sigma-Delta Modulators," 1990 IEEE Int'l Symposium on Circuits and Systems Vol. 4, New Orleans, LA (May 1-3, 1990), pp. 3267-3270.
<i>HFW</i>	AF	Baird, R.T., "Stability Analysis of High-Order Delta-Sigma Modulation for ADC's," IEEE Transactions on Circuits and Systems—II: Analog and Digital Signal Processing, Vol. 41, No. 1, pp. 59-62, January 1994.
<i>HFW</i>	AG	Candy, J.C., "A Use of Double Integration in Sigma Delta Modulation," IEEE Transactions on Communications, Vol. Com-33, No. 3, pp. 249-258, March 1985.
<i>HFW</i>	AH	Noriega, G., "Sigma-Delta A/D Converters – Audio and Medium Bandwidths," RMS Instruments Data Recording Systems Technical Notes – DT3, pp. 1-7, February 1996.
<i>HFW</i>	AI	A/D and D/A Conversion/Sampling Circuits Temperature Sensors, Maxim Appl. Note 1870, "Demystifying Sigma-Delta ADCs," publ'd online, 2003, at: www.maxim-ic.com/an1870 , pp. 1-15. <i>No money</i>
<i>HFW</i>	AJ	Stikvoort, E.F., "Some Remarks on the Stability and Performance of the Noise Shaper or Sigma-Delta Modulator," IEEE Transactions on Communications, Vol. 36, No. 10, pp. 1157-1162, October 1988.

EXAMINER <i>Amir L. Williams</i>	DATE CONSIDERED <i>16 March 2005</i>
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

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HRW	AK	Hussein, A.I., "Bandpass $\Sigma\Delta$ Modulator Employing Undersampling of RF Signals for Wireless Communication," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 47, No. 7, pp. 614-620, July 2000.
HRW	AL	Shoaei, O., et al., "Design and Implementation of a Tunable 40 MHz-70 MHz Gm-C Bandpass $\Delta\Sigma$ Modulator," IEEE Transactions on Circuits and Systems—II: Analog and Digital Signal Processing, Vol. 44, No. 7, pp. 521-530, July 1997.
HRW	AM	Salo, T., et al., "A 80-MHz Bandpass $\Delta\Sigma$ Modulator for a 100-MHz IF Receiver," IEEE Journal of Solid-State Circuits, Vol. 37, No. 7, pp. 798-808, July 2002.
HRW	AN	Yang, C.C., et al., "Transfer Function Design of Stable High-Order Sigma-Delta Modulators with Root Locus Inside Unit Circle" (2002), publ'd online at: AP-ASIC PACIFIC, Conf. Proceedings 2002, 4 pages <i>Not mentioned</i>
HRW	AO	Leuciuc, A., "Nonlinear stabilization techniques for DS modulators: A comparison", Proceedings of 1999 European Conference on Circuit Theory and design ECCTD'99, Stresa, Italy, 29 Aug.-2 Sep. 1999, vol. 2, pp. 679-682.
HRW	AP	Okamoto, T., "A Stable High-Order Delta-Sigma Modulator with an FIR Spectrum Distributor," IEEE Journal of Solid-State Circuits, Vol. 28, No. 7, pp. 730-735, July 1993.

***References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26.

***Note First Page ONLY Header/Footer. Subsequent pages must ONLY have page # layout as header

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